

WEST Search History

DATE: Tuesday, February 22, 2005

<u>Hide?</u>	<u>Set Name</u>	<u>Query</u>	<u>Hit Count</u>
	<i>DB=USPT; PLUR=NO; OP=ADJ</i>		
<input type="checkbox"/>	L20	simultaneous\$2 with prefix with decod\$3	8
<input type="checkbox"/>	L19	(prefix same.decod\$3) and L18	17
<input type="checkbox"/>	L18	l16 and l17	50
<input type="checkbox"/>	L17	712/212.ccls.	194
<input type="checkbox"/>	L16	712/210.ccls.	358
<input type="checkbox"/>	L15	US-5948096-A.did.	1
<input type="checkbox"/>	L14	US-6418527-B1.did.	1
	<i>DB=EPAB,JPAB,DWPI,TDBD; PLUR=NO; OP=ADJ</i>		
<input type="checkbox"/>	L13	comput\$3 and length and instruction\$1 and prefix	14
<input type="checkbox"/>	L12	comput\$3 with length with instruction\$1 with prefix	1
<input type="checkbox"/>	L11	comput\$3 with length with instruction\$1 with identification segment\$1	0
	<i>DB=PGPB,USPT; PLUR=NO; OP=ADJ</i>		
<input type="checkbox"/>	L10	comput\$3 with length with instruction\$1 with identification segment\$1	0
<input type="checkbox"/>	L9	comput\$3 with length with instruction\$1 with prefix	22
<input type="checkbox"/>	L8	length with L7	20
<input type="checkbox"/>	L7	instruction packet\$1	332
<input type="checkbox"/>	L6	instruction with prefix with packet with length	1
<input type="checkbox"/>	L5	instruction with prefix with vliw with length	0
<input type="checkbox"/>	L4	instruction prefix with vliw length	0
<input type="checkbox"/>	L3	(decod\$3 same parallel same x86 same instructions) and advanced.as.	92
<input type="checkbox"/>	L2	rotators.ti. and grochowski.in.	1
<input type="checkbox"/>	L1	rotaters.ti. and grochowski.in.	0

END OF SEARCH HISTORY